12-13-04

ATTORNEY DOCKET 068354.1468

PATENT APPLICATION 09/870,460

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Michael I. Catherwood

Serial No.:

09/870,460

Filing Date:

June 1, 2001

Group Art Unit:

2131

Examiner:

Christopher A. Revak

Title:

Modified Harvard Architecture Processor Having Data Memory Space Mapped to Program Memory

Space with Erroneous Execution Protection

Mail Stop Amendment Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450 I hereby certify that this Information Disclosure Statement is deposited with the United States Postal Service as Express Mail EV351287178US addressed to: Mail Stop Amendment, Commissioner of Patents, Alexandria, VA 1450, on the date shown below

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. Copies of the references are enclosed for the convenience of the Examiner. Furthermore, pursuant to 37 C.F.R. §§1.97 (g) and (h), no representation is made that these references are material to the patentability of the present application.

Applicant believes no fees are due, however, the Commissioner is hereby authorized to charge any additional fees or credit any overpayments to Deposit Account No. 50-2148 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTTS L.L.P. Applicant

Bruce W. Slayden I Reg. No. 33,790

Date:

Correspondence Address:

Customer Number 31625

512.322.2606

512.322.8306 (Fax)

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